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IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

Attorney Docket No.: **Poly-22-2/APP**

Applicants: **Eiji OKI et al.**

Serial No.: **09/911,038**

Filing Date: **July 23, 2001**

Title: **SCHEDULING THE DISPATCH OF CELLS IN NON-EMPTY VIRTUAL OUTPUT QUEUES OF MULTISTAGE SWITCHES USING A PIPELINED HIERARCHICAL ARBITRATION SCHEME**

Examiner: **Not yet assigned**

Group Art Unit: **Not yet assigned**

Assistant Commissioner for Patents
Washington, D.C. 20231

S I R:

Information Disclosure Statement

The applicants respectfully request that the references listed on the attached PTO-FORM-1449 be considered in the examination of the above-identified application. A copy of each of these references is enclosed.

The applicants preserve the right to establish that any of the references listed on the attached PTO-FORM-1449 are not prior art to the above-captioned application.

Respectfully submitted,

Dated: August 17, 2001

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CERTIFICATE OF MAILING under 37 C.F.R. 1.8(a)

I hereby certify that this correspondence is being deposited on **August 17, 2001** with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

John C. Pokotylo — 36,242
John C. Pokotylo — Reg. No.

(modified PTO-1449)

U.S. Department of Commerce
Patent and Trademark Office

LIST OF REFERENCES CITED BY APPLICANT
(use as many sheets as necessary)

Sheet	1	of	1

Complete if Known

Application Number:	**09/911,038**
Filing Date:	**July 23, 2001**
First Named Inventor:	**Eiji OKI**
Group Art Unit:	**Not yet assigned**
Examiner Name:	**Not yet assigned**
Attorney Docket No.:	**Poly-22-2/APP**

OTHER REFERENCES - NON-PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume, issue number(s), publisher, country, where published, source	T[2]
	AA.	N. W. McKeown, "Scheduling Algorithms for Input-Queued Cell Switches," PhD Thesis, University of California at Berkeley, (1995).	
	AB.	C. Y. Lee and A. Y. Oruç, "A Fast Parallel Algorithm for Routing Unicast Assignments in Benes Networks," IEEE Trans. on Parallel and Distributed Sys., Vol. 6, No. 3, pp. 329-334 (March 1995).	
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	AI.	G. Nong, J. K. Muppala and M. Hamdi, "Analysis of Nonblocking ATM Switches with Multiple Input Queues," IEEE/ACM Transactions on Networking, Vol. 7, No. 1, pp. 60-74 (Feb. 1999).	
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	AS.	A. Smiljanić, "Flexible Bandwidth Allocation in Terabit Packet Switches," pp. 233-239.	

Examiner Signature		Date Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Unique citation designation number. 2 Applicant is to place a check mark here if English language translation is attached.